



Paul Chow - nominated by IEEE Canada

Professor Paul Chow has made several key contributions to computer architecture as a researcher, educator and industry leader. As a Research Associate at Stanford University in the 1980s, he was a major contributor to an early RISC microprocessor design called MIPS-X, one of the first microprocessors with an on-chip instruction cache and the root of many concepts used in processors today.

Since joining the Department of Electrical and Computer Engineering at the University of Toronto in 1988, he has performed groundbreaking research on field programmable gate array (FPGA) architectures and their applications in fields from biomedicine to finance. He has also developed and taught innovative courses on integrated chip design resulting in student-designed chips that have furthered research in the field.

For his remarkable contribution to computer architecture, we now pay tribute to Paul Chow with these honours.

Ladies & gentlemen and Madam President, please welcome Paul Chow as a Fellow of the Engineering Institute of Canada.